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REPLACEMENT SHEET

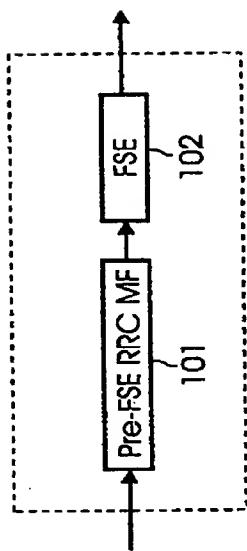
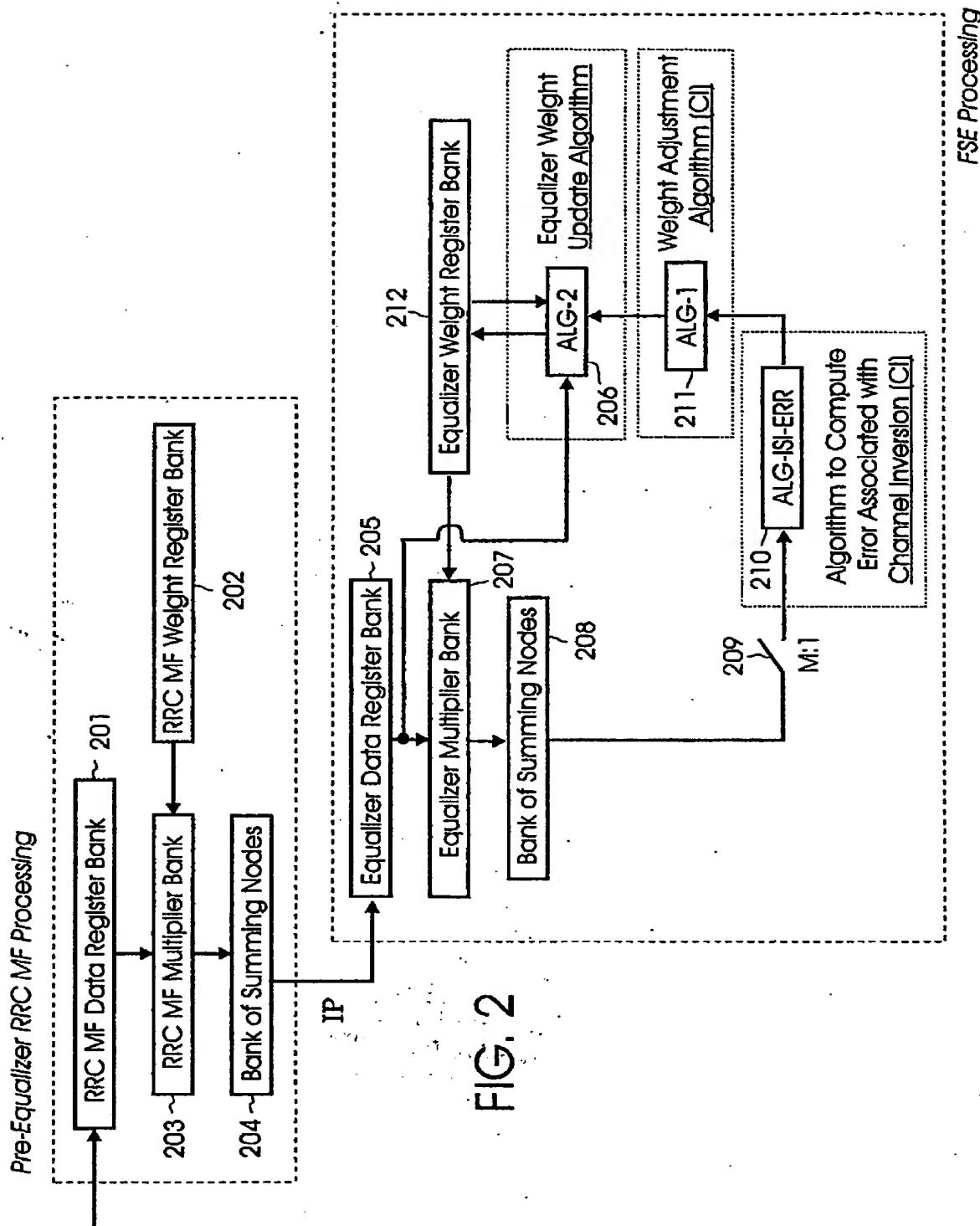


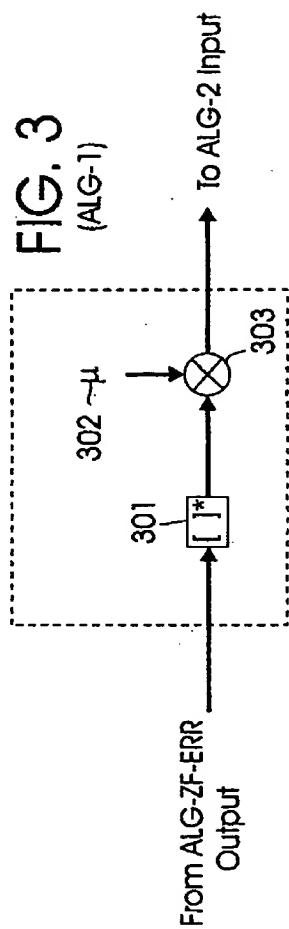
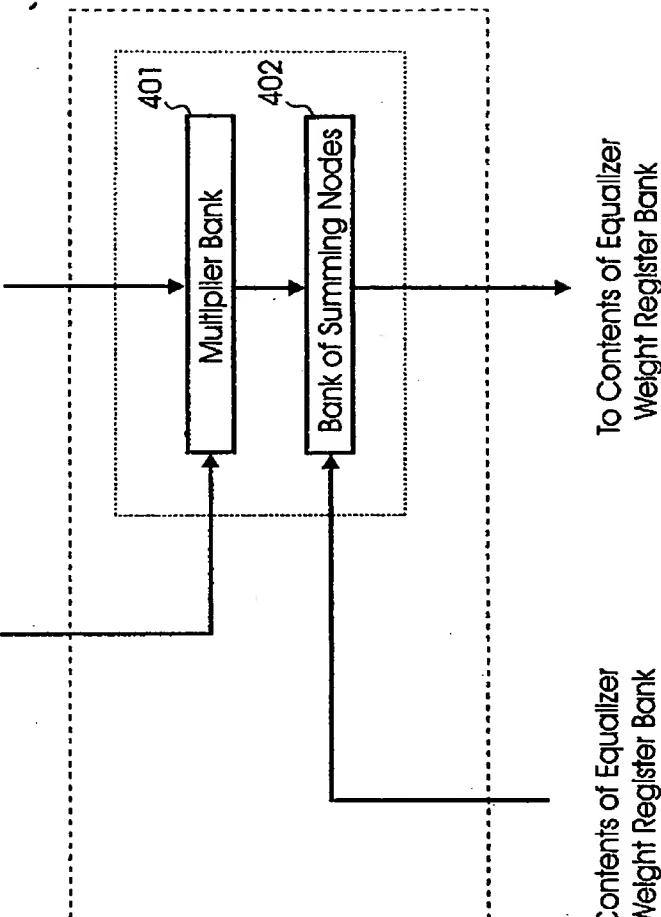
FIG. 1



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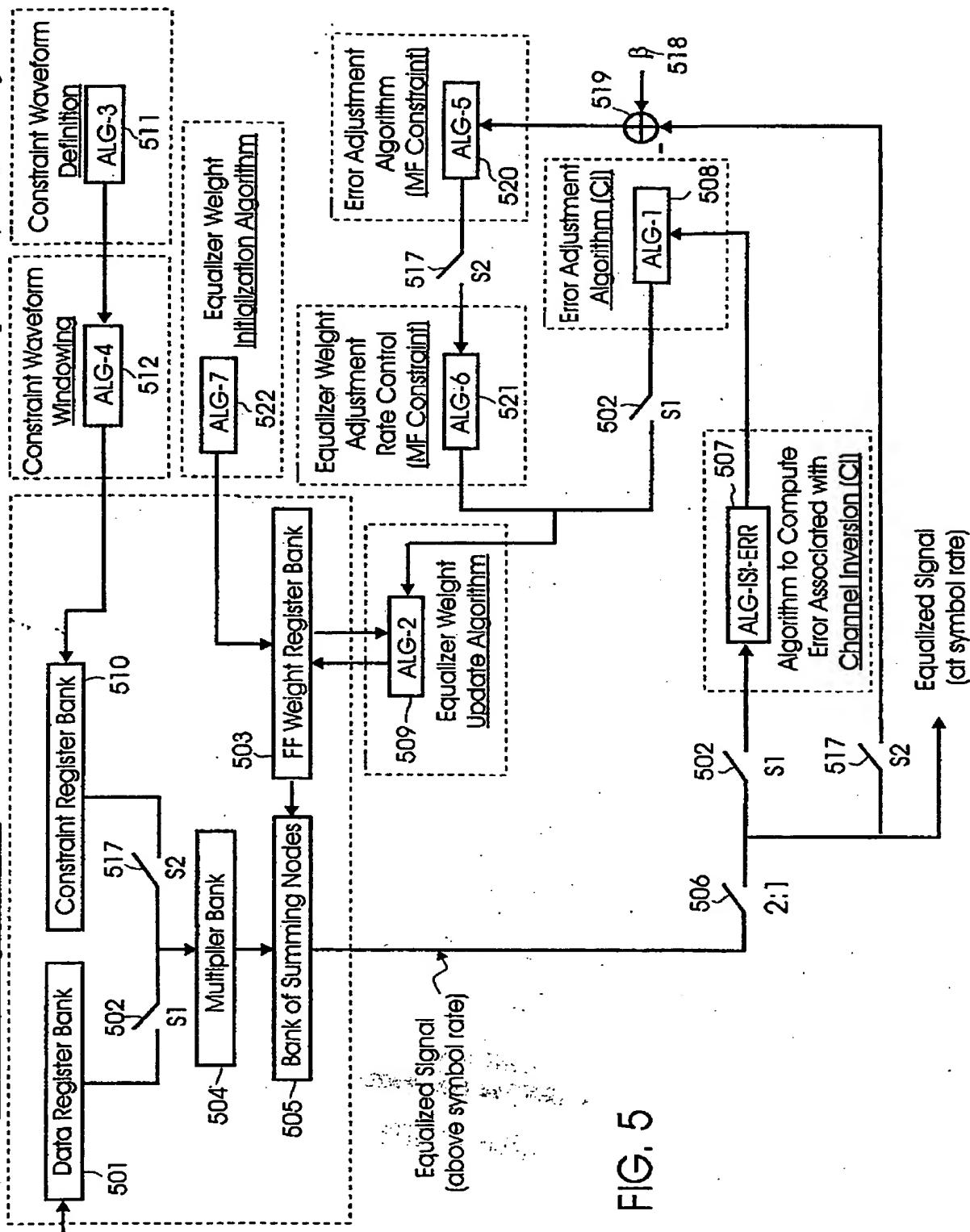
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FIG. 3
(ALG-1)From ALG-1
OutputContents of Equalizer
Data Register BankFrom ALG-1
OutputContents of Equalizer
Data Register BankTo Contents of Equalizer
Weight Register BankContents of Equalizer
Weight Register BankFIG. 4
(ALG-2)

REPLACEMENT SHEET

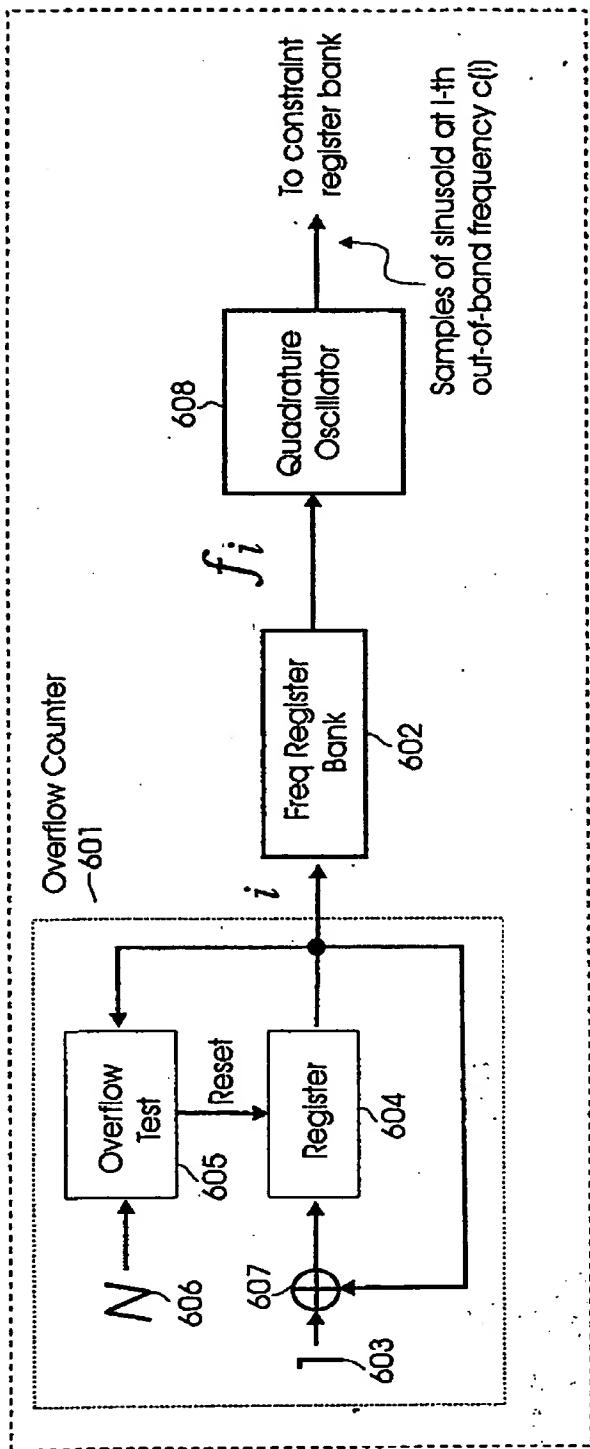
Time-multiplexing Architecture

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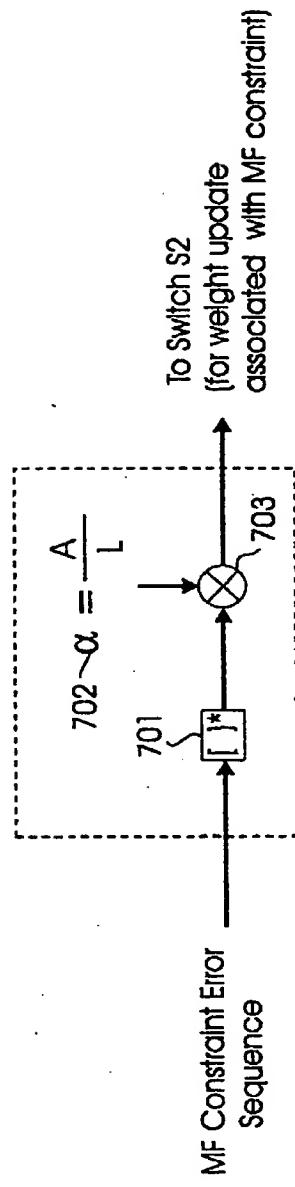


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EIG

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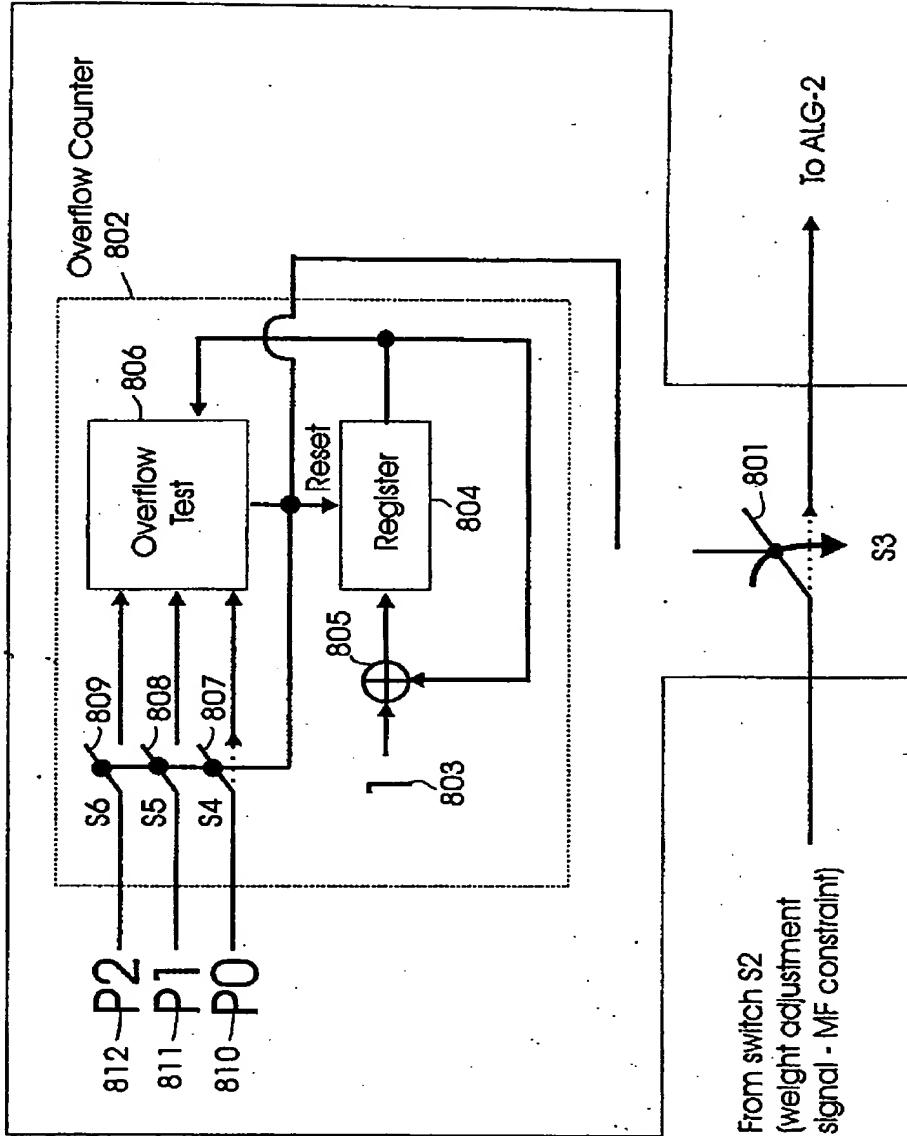


[ALG-3]
FIG. 6



[ALG-5]
FIG. 7

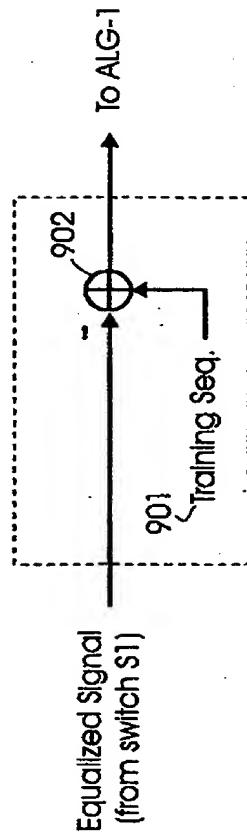
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[ALG-6]
FIG.8

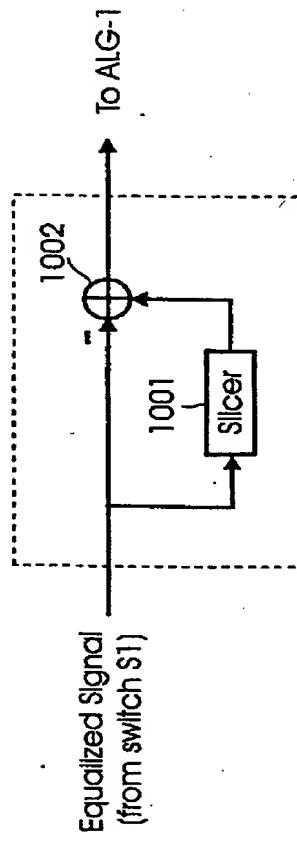
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{ALG-ISI-ERR - Training Sequence Emb.}

FIG. 9



{ALG-ISI-ERR - Decision-Directed Emb.}

FIG. 10

REPLACEMENT SHEET

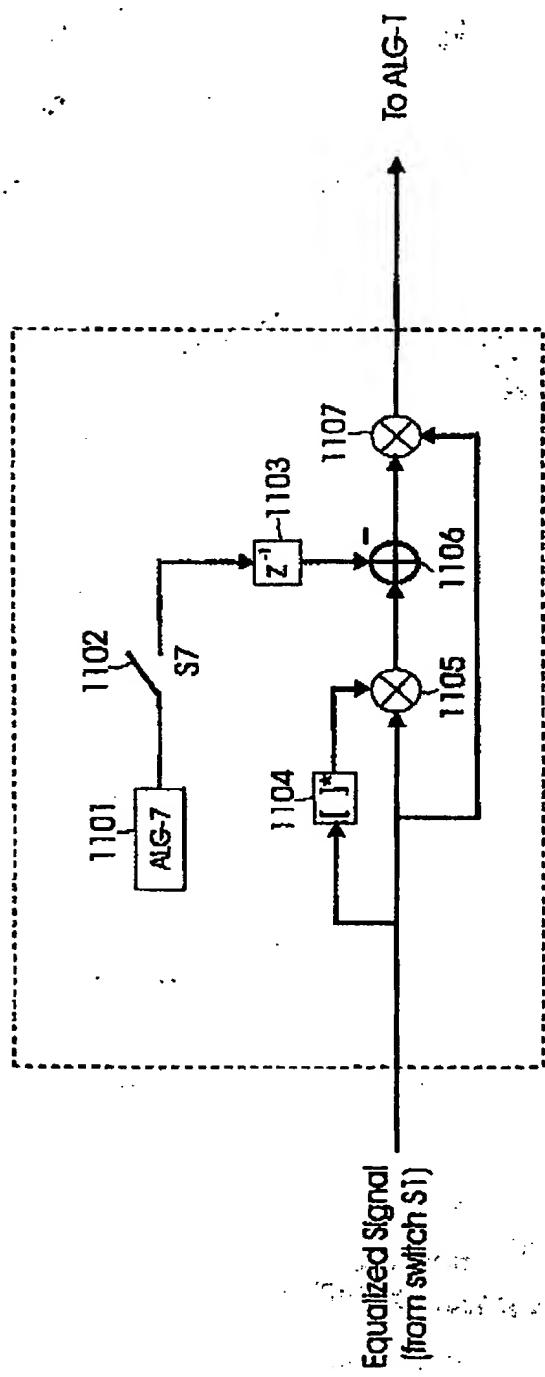
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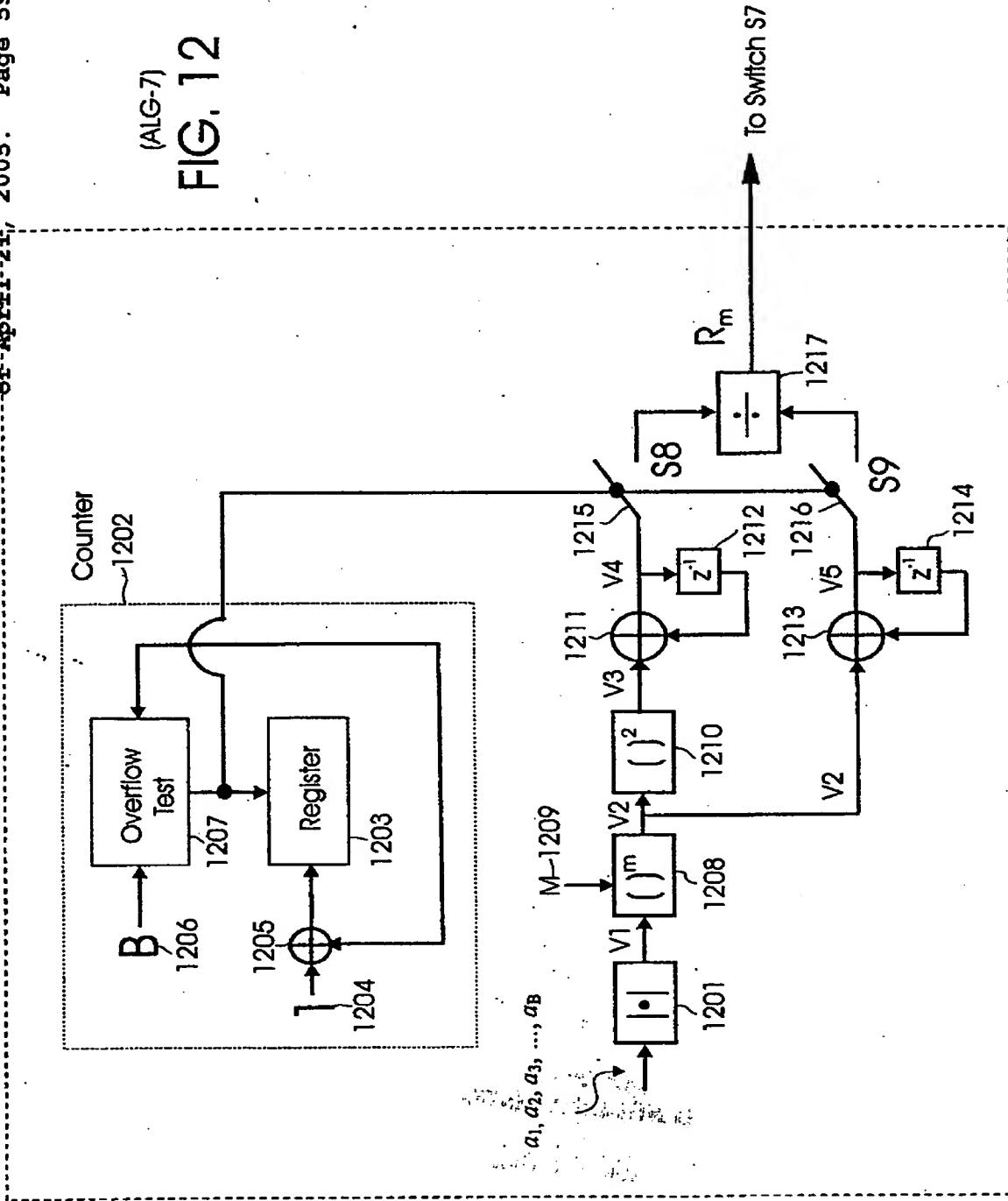


[ALG-Z-ERR Blind CMA Emb.]

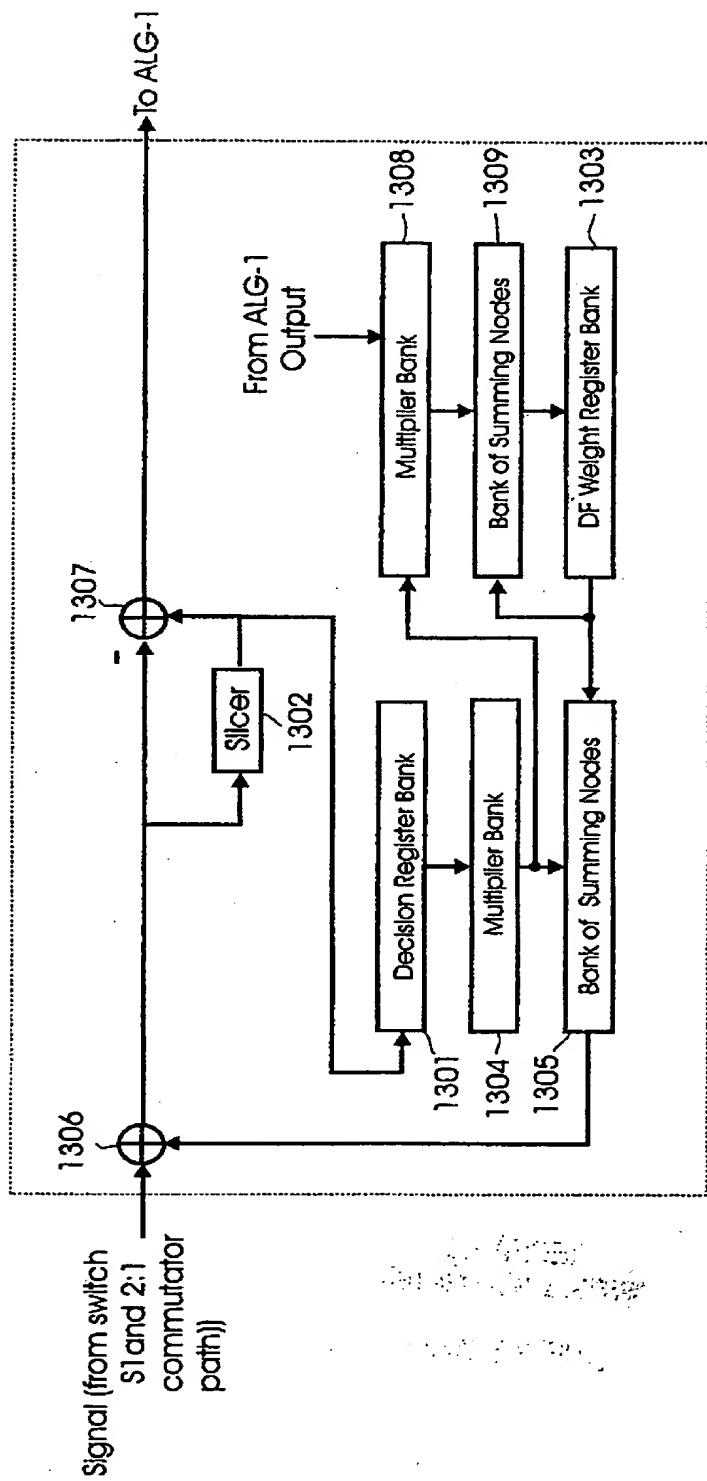
FIG. 11

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(ALG-7)
FIG. 12



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{ALG-7 - Decision-Feedback Emb.}

FIG. 13

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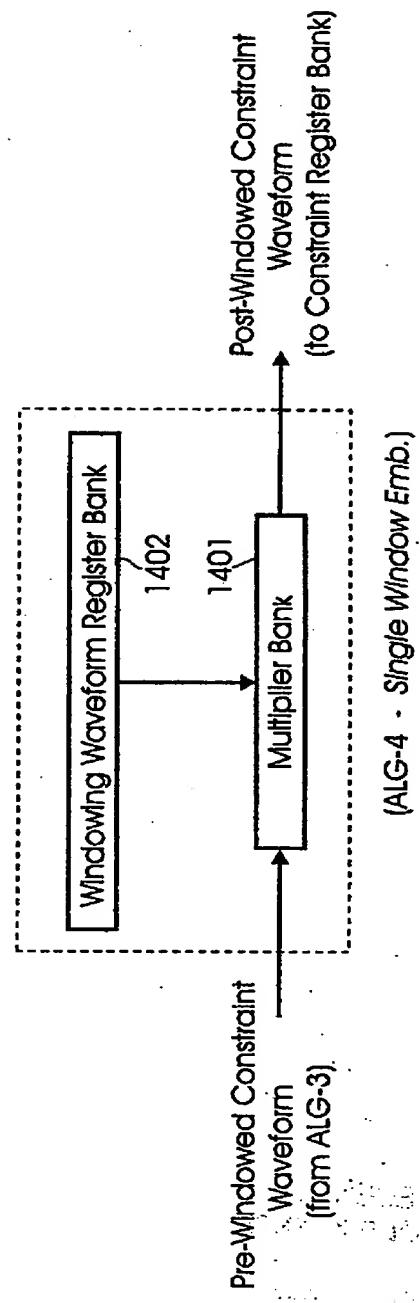
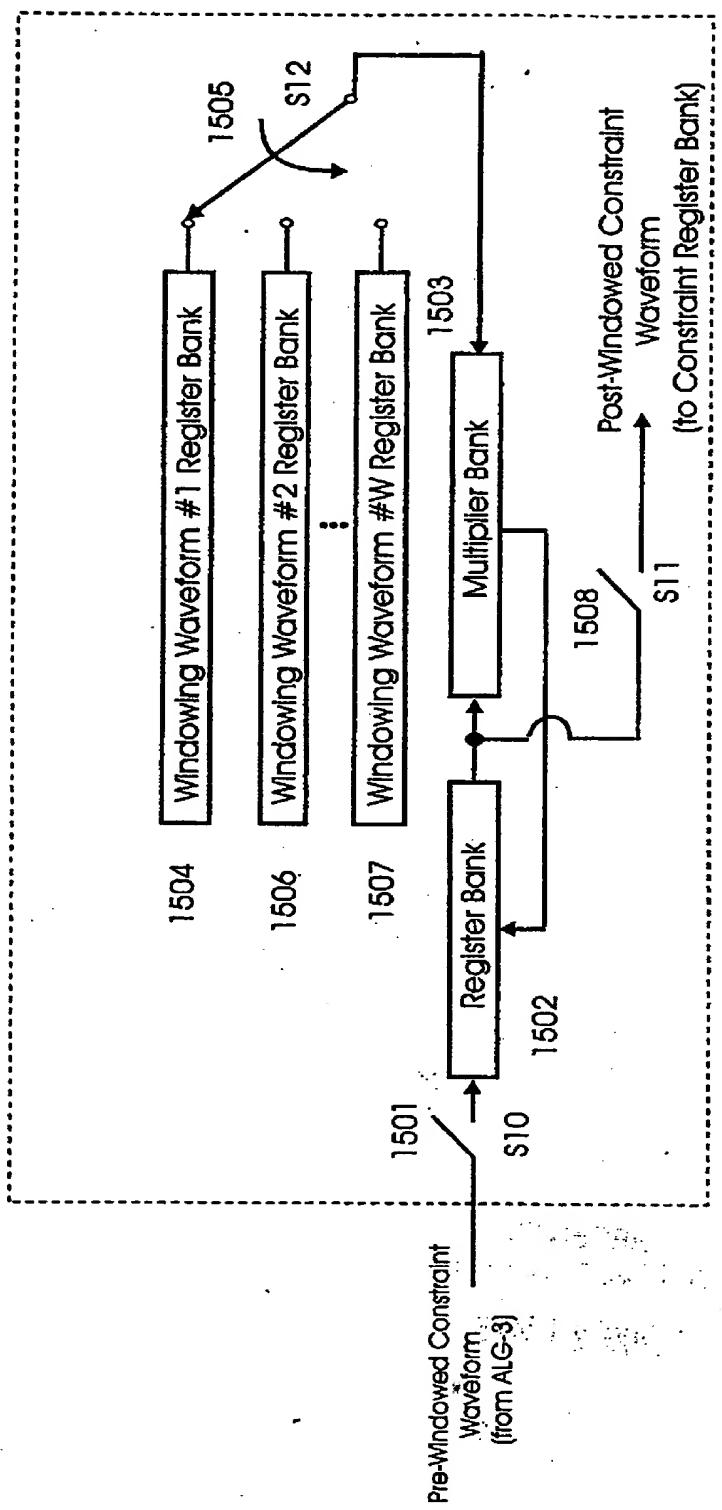


FIG. 14

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(ALG-4 - Multi-Window Emb.)

FIG. 15

REPLACEMENT SHEET

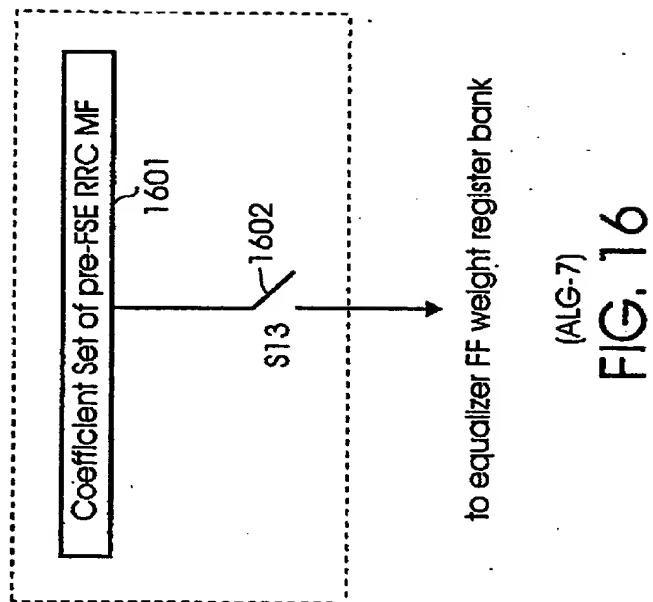
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